

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	5022	differential adj amplifier and (analog or sinusoidal) adj signal	USPAT; EPO; JPO; DERWENT	2002/09/10 10:38
2	L2	1480	1 and semiconductor	USPAT; EPO; JPO; DERWENT	2002/09/10 10:38
3	L4	0	3 and distribution adj2 (tree or circuit)	USPAT; EPO; JPO; DERWENT	2002/09/10 10:42
4	L3	287	2 and (327/\$.ccls. or 330/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2002/09/10 11:08
5	L5	262	3 and transistor\$1	USPAT; EPO; JPO; DERWENT	2002/09/10 11:08
6	L6	75	5 and clock adj signal	USPAT; EPO; JPO; DERWENT	2002/09/10 11:10
7	L7	187	5 not 6	USPAT; EPO; JPO; DERWENT	2002/09/10 13:38
8	L8	47	distributi\$2 near2 clock adj signal and clock adj tree	USPAT; EPO; JPO; DERWENT	2002/09/10 13:40
9	L9	1	8 and sinusoidal	USPAT; EPO; JPO; DERWENT	2002/09/10 15:57
10	L10	3641	clock adj2 (generator or generating) and 327/\$.ccls.	USPAT; EPO; JPO; DERWENT	2002/09/10 16:25
11	L11	184	10 and (sinusoidal or standing adj waves)	USPAT; EPO; JPO; DERWENT	2002/09/10 15:58
12	L12	27	11 and distribution	USPAT; EPO; JPO; DERWENT	2002/09/10 16:01
13	L13	157	11 not 12	USPAT; EPO; JPO; DERWENT	2002/09/10 16:09
14	L14	0	13 and clock adj tree	USPAT; EPO; JPO; DERWENT	2002/09/10 16:08
15	L15	2	13 and distributing	USPAT; EPO; JPO; DERWENT	2002/09/10 16:09
16	L17	98	clock adj2 (generator or generating) and (sinusoidal or standing waves) and clock near2 distributi\$3	USPAT; EPO; JPO; DERWENT	2002/09/10 16:27
17	L18	86	17 not 11	USPAT; EPO; JPO; DERWENT	2002/09/10 16:27

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4	L3	287	2 and (327/\$.ccls. or 330/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2002/09/10 11:08
5	L5	262	3 and transistor\$1	USPAT; EPO; JPO; DERWENT	2002/09/10 11:08
6	L6	75	5 and clock adj signal	USPAT; EPO; JPO; DERWENT	2002/09/10 11:10
7	L7	187	5 not 6	USPAT; EPO; JPO; DERWENT	2002/09/10 13:38
8	L8	47	distributi\$2 near2 clock adj signal and clock adj tree	USPAT; EPO; JPO; DERWENT	2002/09/10 13:40